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Patent	Application Number	09/927,599					
Address to: Mail Stop Post Issue	Filing Date 08/09/2001						
Commissioner for Patents P.O. Box 1450	First Named Inventor Cheng, Paul C.						
Alexandria, VA 22313-1450	Attorney Docket Number	ALNC01500	ALNC01500				
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STATEMENT UNDER 37 CFR 3.73(b)	
Applicant/Patent Owner. Integrated Silicon Solution, Inc.	
Application No./Patent No.: 6,889,225 Filed/Issue Date: issued 05/03/2005	
Entitled: Segmented Bus Architecture For Improving Speed In Integrated Circuit Memories	
Integrated Silicon Solution, Inc. , a Corporation (Name of Assignee)	_
(Type of Assignee, e.g., corporation, partnership, university, govern states that it is:	ment agency, etc.)
1. The assignee of the entire right, title, and interest; or	
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Cheng, Paul et al. To: Purple Ray, Inc. The document was recorded in the United States Patent and Trademark Office at Reel 012987 , Frame 0843 , or for which a copy thereof is attached.	
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[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to As- Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. Sec 302.08]	signment MPEP
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Signature Date	
Lou Yang 408-969-51;	24
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